Name: Dr. P. Ramakrishna

Received his B. Tech degree in Electronics and communications engineering from NIT Warangal, India. M. Tech degree in VLSI System Design from JNT University affiliated college, Hyderabad India. 15 years of teaching and research experience. Completed Ph. D at K.L.E.F (Deemed to be University), Guntur, Andhra Pradesh, India. Presently working as an Assistant Professor, Anurag University, Hyderabad, Telangana. And published more than 50 International Journal papers, International conferences and published two patents. His research interests include VLSI System Design, IOT, Low power VLSI, and Device modeling.