**Dr.Anu.G.Pillai**

anupillai1001@gmail.com

Mob: +91 9770542576



 **Address:**

 House No: 54, Housing Board Colony

 BMY(po), Charoda, Durg(Distt)

 Chhattisgarh-490025

**OBJECTIVE:**

Seeking a position to utilize my experience of 17 Years in electrical engineering that offers professional growth while being resourceful, innovative and flexible.

**EDUCATIONAL QUALIFICATIONS:**

**BRANCH: ELECTRICAL ENGINEERING**

**(PhD – POWER SYSTEM**  **ENGINEERING)**

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| **DEGREE**  | **COLLEGE/SCHOOL**  | **BOARD/UNIVERSITY**  | **YEAR**  |
| PhD | KALINGA UNIVERSITY  | KALINGA, RAIPUR UNIVERSITY  | 2024 |

 **(M-Tech – POWER SYSTEM**  **ENGINEERING)**

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| **DEGREE**  | **COLLEGE/SCHOOL**  | **BOARD/UNIVERSITY**  | **YEAR**  | **%**  |
| M.-TECH | SSTC/SSGI/FET,JUNWANI,BHILAI  | CSVTU, BHILAI (C.G)  | 2014  | 8.6(CPI)  |

**(BE - ELECTRICAL ENGINEERING)**

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| **DEGREE**  | **COLLEGE/SCHOOL**  | **BOARD/UNIVERSITY**  | **YEAR**  | **%**  |
| B.E | CCET, Kailash nagar,BHILAI | Pt. R.S.S.U,Raipur (C.G.)  | 2007  | 7.4(CPI)  |

**(BE - ELECTRICAL ENGINEERING)**

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| **DIPLOMA**  | **COLLEGE/SCHOOL**  | **BOARD/UNIVERSITY**  | **YEAR**  | **%**  |
| Engineering Diploma | Govt Polytechnic College, Durg (CG) | Rajiv Gandhi Prodhyogiki Vishwavidyalay , Bhopal | 2004 | 61.74% |

**SCHOOLING**

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| **CLASS**  | **SCHOOL**  | **BOARD/UNIVERSITY**  | **YEAR**  | **%**  |
| 10th  | JYOTI VIDYALAY ENG MED HR.SEC. SCHOOL, CHARODA  | C.G BOARD  | 2001  | 71.4%  |

**TEACHING EXPERIENCE:**

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| **UG/PG**  | **COLLEGE**  | **DURATION**  | **TOTAL EXPERIENCE**  |
| B-Tech | NIT Raipur (Ad-Hoc faculty) | June 2007- January 2008 | 6 months |
| B-Tech/M-Tech  | SSTC/SSGI/FET,JUNWANI BHILAI.  | From March 2008 to November 2022  | 15 years  |
| B-Tech/M-Tech/Diploma | Kalinga University, Raipur | From December 2022 till date | 1.3 years |

**Google Scholar Id:** <https://scholar.google.com/citations?user=mk_SoUsAAAAJ&hl=en>

**WORKSHOPS / CONFERENCES:**

* Attended Five days workshop on “**ARTIFICIAL INTELLIGENCE**” under “ATAL ACADEMY” organized by “BHILAI INSTITUTE OF TECHNOLOGY”

Durg from 18th January 2021 to 22nd January 2021.

* Attended Five days workshop on “**POWER SYSTEM RALIABILITY**” under “ATAL ACADEMY” organized by “NIT, Delhi” from 26th October 2020 to 30th October 2020.
* Attended one day workshop on topic ”Challenges in Gender Equality & Women Empowerment” in Electrical Engineering Department of SSTC under the banner of TEQIP III, CSVTU on 27th September 2019.
* Attended one day workshop on “Women Empowerment:Awareness on Safety & Legal Issues” under the banner of TEQIP III, CSVTU at SSTC, Bhilai, on 28th  August 2019.
* Attended one day workshop on “Motivational Empowerment & Stress Management” under the banner of TEQIP III, CSVTU at SSTC, Bhilai, on 11th  June 2019.
* Attended two days workshop on “Virtual Laboratory” jointly organized by IIT, Bhilai and IIT,Guwahati on 26th and 27th March2019.
* Attended five days workshop on “Materials Modelling for Device Applications” in Applied Physics Department of SSTC under the banner of TEQIP III, CSVTU at SSTC, Bhilai, from 18th to 23rd February 2019.
* Attended two days workshop on “Renewable & Sustainable Technologies for Power Generation” in Electrical Engineering Department of BIT under the banner of TEQIP III, CSVTU at Bhilai Institute of Technology, Durg, from 14th and 15th February 2019.
* Attended five days workshop on “Research opportunities in Artificial Intelligence and Machine learning ” in Applied Physics Department of SSTC under the banner of TEQIP III, CSVTU at SSTC, Bhilai, from 17th to 21st December 2018.
* Attended five days FDP on “Upcoming Trends & Challenges in Electrical Engineering” sponsored by AICTE under Margdarshan scheme and Bhilai Institute of Technology, Durg, from December 10th to 14th 2018.
* Participated in national conference on “Innovative advances in control & power engineering” in BIT, Durg held during 20 & 21 February 2015.
* Attended two days workshop on “Value Education” organized by CSVTU, Bhilai on 26th and 27th July 2013.
* Participated in FDP on “Advancement in power system and power electronics” in CSIT, Durg held during 29th April to 11th May2013.
* Attended “Staff Development Programme” organized by NITTTR, Bhopal under Government of India, Ministry of Human Resource Development on 16th to 18th December 2009.
* Attended workshop on “Instrumentation through LABVIEW” in SSCET, Bhilai held on 3rd December 2008.
* Participated and presented paper in national conference on “Technologia” held in MPCCET, Bhilai during 2nd& 3rd March 2007.

**PUBLICATIONS:**

* Published a **SCOPUS** research paper entitled “Automatic Solar Tracking System” in **Journal of Pharmaceutical Negative Results,** eISSN: 0976-9234, Volume 14, Special Issue 2, January 2022.
* Published a **SCOPUS**research paper entitled “Crime Detection Approach Using Big Data Analytics and Machine Learning” in**NeuroQuantology,** eISSN: 1303-5150, Volume 20, Issue 8, May2022.
* Published a research paper entitled “Comprehensive Analysis of P & O Based MPPT Techniques for Solar PV System” in **GIS SCIENCE JOURNAL,** Online ISSN: 1869-9391, Volume 9, Issue 6, June 2022.
* Published a research paper entitled “Design AND Simulation of Grid Connected Photovoltaic System based on the MPPT Strategy” in **International Journal of Scientific Research in Engineering and Management (IJSREM),** Online ISSN: 2582-3930, Volume 6, Issue 6, June 2022.
* Published a research paper entitled “A Survey on Renewable Power Resources Prediction Techniques” in **DICKENSIAN JOURNAL,** Online ISSN: 0012-2440, Volume 22, Issue 7, July 2022.
* Published a research paper entitled “Renewable Energy Load Balancing

 Using Grey Wolf Genetic Algorithm” in **DICKENSIAN JOURNAL,** Online ISSN: 0012-2440, Volume 22, Issue 7, July 2022.

* Published a research paper entitled “Study on Power Quality Problem and its Mitigation Techniques in Electrical power system” in **International Research Journal of Engineering and Technology (IRJET),** Print ISSN: 2395-0072, Online ISSN: 2395-0056, Volume 6, Issue 10, 10th October 2019

**EXTRA ACHIEVEMENTS :**

* Published 4 patents.
* Published two Book Chapters and Undergoing one book chapter.
* Bagged the position of 2nd topper in university exam in merit list of Electrical Engg in CCET (Pt.R.S.SU) 2007.
* Won the scholarship (St.Thomas Merit award) from the college management (MPCCET) for being in the second position during final year of engineering.
* Undergone Bridge Program covering computer fundamentals in 3 Edge Solutions, Chennai.

**COMPUTING SKILLS:**

Software skills - C++ Basics, M.S Word, Excel, Power point

**PERSONAL DETAILS:**

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| Date of Birth  | :  | 30th October 1985  |
| Father’s Name  | :  | Mr. G.K.Pillai |
| Mother’s Name  | :  | Mrs. Retnamma.P.G |
| Hobbies  | :  | Music  |
| Strength  | :  | Self motivated, Dedicated towards Hard work.  |
| Nationality  | :  | Indian  |
| Sex Languages Know  | : :  | Female English , Hindi , Malayalam  |
| **DECLARATION :**  |  |  |

 I vouch for the authenticity of the above information as furnished by me to be true and correct to the best of my knowledge and belief, till date.

**Date:** 4/04/2024

**Place** : BHILAI **ANU.G.PILLAI**