Dr. PRIYANKA GUPTA

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OBJECTIVE

To secure a position where I can effectively contribute my skills that offers professional growth while being resourceful and innovative.

WORK EXPERIENCE

* Presently working as Assistant Professor at Bharati Vidyapeeth's College of Engineering, Delhi, India from August 2021.
* Worked at the post of Assistant Professor (Guest Faculty) at Bharati Vidyapeeth's College of Engineering, Delhi, India for session October 2020- December 2020.
* Worked at the post of Assistant Professor (Guest Faculty) at Delhi Technological University (ECE Dept.), Delhi for session January-June 2018. August-December 2018 and January-June 2020.
* Worked at the post of Assistant Professor at National Institute of Technology, Delhi on contractual basis in July-August2016.
* Worked as a Junior Research Fellow at Northern India Engineering College, Shastri Park, Delhi, on a research project of DST sponsored project from January 2016 to July 2016.
* Worked at the post of Assistant Professor at PDM College of Engineering for Women (ECE Dept.), Bahadurgarh, affiliated to M.D.U., Rohtak from July 2011 to July 2014.

SUBJECTS TAUGHT

* Communication Systems
* Digital Communication
* Digital Electronics
* Analog Electronics
* Signals and systems

SPECIALIZATION/ AREA OF INTEREST

Thesis Title: Design of Continuous Time Circuits using Current Mode Building Blocks

* Analog Signal Processing
* Analog Circuit Design
* Analog Integrated Circuits

SCHOLASTIC ACHIEVEMENTS

* Received Best Researcher Award from Bharati Vidyapeeth’s College of Engineering, New Delhi in the year 2023.
* Received Best Researcher Award from Bharati Vidyapeeth’s College of Engineering, New Delhi in the year 2022.
* Received Commendable Research Award from Delhi Technological University in the year 2020.
* Won first prize, merit certificate and cash prize for best poster presentation at NFEST-2018, held at DTU, Delhi.
* Batch Topper M.Tech, ECE branch, VCE, Rohtak.
* Received Letter of appreciation twice from PDMCEW, Bahadurgarh for bringing 100% result for the subject taught in a session.

PATENTS PUBLISHED

Title of the invention: **VDBA Based First Order All Pass Filter and Its Applications**, Dated- 02 July 2021.

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| Examination | University/Board | Year of Study | Percentage/CGPA |
| Ph.D. | Delhi Technological University, Delhi | 2014-2020 | NA |
| M.TECH.(ECE) | Maharshi Dayanand University, Rohtak | 2009-11 | 84.17 |
| B.E. (HONS.) (ECE) | Maharshi Dayanand University, Rohtak | 2004-08 | 77.45 |
| XII | C.B.S.E. | 2003-04 | 79.80 |
| X | C.B.S.E. | 2001-02 | 74.80 |

EDUCATION

PUBLICATIONS IN JOURNALS

1. Parveen Rani, P.Gupta, K. Gurumurthy, R Pandey “New Approaches for Realizing Transconductance-boosted VDTA Structures” Accepted for publication in, Journal of Circuits, Systems, and Computers (Jan 2024) (SCI Indexed).
2. J Gupta and P Gupta, “Human Health and Real-time Nitric Acid Detection in Drinking” Accepted for publication in, International Journal of Information Technology (Sep 2023) (Scopus Indexed).
3. P Gupta, Surbhi Gupta, Yashika Aggarwal, Utkarsh and Shruti, “A new Adaptive biased VDTA” Journal of Circuits, Systems, and Computers (July 2023) (SCI Indexed).
4. P.Gupta and R. Pandey, “Low Power Squaring Circuit Using Single Voltage Differencing Buffered Amplifier”, Journal of Circuits, Systems, and Computers (**June 2022) (SCI Indexed).**
5. K. Gurumurthy and **P. Gupta** “A New Realization of Third-Order Inverse Bandpass Filter and Its Application as an Oscillator”, Journal of Circuits, Systems, and Computers, Vol. 31, No. 5, **2022,** pp. 2250079.1- 2250079.20 (**SCI Indexed**).
6. **P.Gupta** and R. Pandey, “Dual Output Voltage Differencing Buffered Amplifier Based Active -C Multiphase Sinusoidal Oscillator”, IJE Transactions C: Aspects, Vol. 32, No. 6, **2021,** pp. 1438-1444 (**ESCI Indexed**).
7. **P.Gupta** and R. Pandey, “A Low Power Voltage Differencing Buffered Amplifier”, International Journal of Circuit Theory and Applications, **2019** (**SCI Indexed**).
8. **P.Gupta** and R. Pandey, “Voltage Differencing Buffered Amplifier based Voltage Mode Four Quadrant Analog Multiplier and its Applications”, IJE Transactions A: Basics, Vol. 32, No. 4, **2019,** pp. 528-535 (**ESCI Indexed**).
9. **P.Gupta,** N. Pandeyand R. Pandey, “High CMRR Wide Bandwidth Instrumentation Amplifier Based on VDBA”, Recent Advances in Electrical & Electronic Engineering, Volume 11, Issue 2, **2018,** pp.239-247 (**ESCI Indexed**).
10. **P.Gupta** and R. Pandey "Single VDBA based Multifunction Filter", International Journal of Control Theory and Applications, Vol. 10, Issue 6, **2017**, pp. 651-­661.
11. K. Gupta, **P.Gupta**, N Pandey and R. Pandey titled “CDBA - Current Based Instrumentation Amplifier” in Journal of Communications Technology, Electronics and Computer Science, Issue 4, pp. 11-15, 2016, ISSN 2457-905X.

PAPERS IN CONFERENCES

1. **P. Gupta** and R. Pandey “A Review of VDBA Implementations and Applications”, at International Conference NFEST- 2018, held at DTU Delhi, 8-12 January **2018**.
2. **P. Gupta** and R. Pandey “Voltage Mode Single CDBA Based Multifunction Filter” at International Conference on Advanced Material Technologies (ICAMT)-2016, held at DIET, Visakhapatnam, 27-28 December **2016**. Proceedings published in IOP Conference Series: Materials Science and Engineering, Volume 225**(Scopus Indexed).**
3. **P. Gupta** and R. Pandey d “A gm-C Quadrature Oscillator based on DO-VDBA” National Conference on Recent Innovations in Engineering and Technology (NCRIET- 2016), Proceedings published in International journal of innovations in engineering and management, pp.- 49-52, vol. 5; no. 1, 2016, ISSN: 2319-3344.

IN COMMUNICATION

Four Manuscripts are communicated to reputed SCI indexed journals.

WORKSHOPS

* Attended a One day workshop on “Tutorial on synthesis of Op-Amp and phase locked loop topologies from first principles and a modern agnostic technique for simulating pre element distortion contributions”, conducted by IEEE on 20 October2014**.**
* Attended national workshop on “PCB designing”, at the Technological Institute of Textile & Sciences, Bhiwani on 20 March 2010.

FACULTY DEVELOPMENT PROGRAMME

* Participated AICTE Training and Learning (ATAL) Academy Online Elementary FDP on "Custom IC Design in the Advanced CMOS Technology" from 21/11/2021 to 25/11/2021 at Indian Institute of Information Technology Pune.
* Participated AICTE Training and Learning (ATAL) Academy Online Elementary FDP on "Design of low power analog integrated circuits" from 29/11/2021 to 03/12/2021 at Gauhati University Institute of Science and Technology.
* Attended faculty development program on “Efficient Engineering Education”, from 03-09 February 2014 at PDMCEW, Bahadurgarh.
* Attended faculty development program on “Efficient Engineering Education”, from 17-22 December 2012 at PDMCEW, Bahadurgarh.
* Attended national workshop on “MATLAB & its applications in Engineering”, Maharishi Markandeshwar University, Sadopur Ambala on 20-21 April, 2012.
* Attended faculty development program on “Teaching methodology in Engineering Education”, from 9-22 July 2011 at PDMCEW, Bahadurgarh.

**REFERENCES**

1. Dr. Rajeshwari Pandey

Professor, ECE Department

Delhi Technological University (Formerly Delhi College of Engineering)

Bawana Road, Delhi-110042

Phone: +91-9968243273

1. Dr. Neeta Pandey

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Delhi Technological University (Formerly Delhi College of Engineering)

Bawana Road, Delhi-110042

Phone: +91-9868780900

(Dr. Priyanka Gupta)

List of Publications

PUBLICATIONS IN JOURNALS

1. J Gupta and P Gupta, “Human Health and Real-time Nitric Acid Detection in Drinking” Accepted for publication in, International Journal of Information Technology (Sep 2023) (Scopus Indexed).
2. **P Gupta,** Surbhi Gupta, Yashika Aggarwal, Utkarsh and Shruti, “A new Adaptive biased VDTA” Accepted for publication in, Journal of Circuits, Systems, and Computers **(June 2023) (SCI Indexed).**
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4. K. Gurumurthy and **P. Gupta** “A New Realization of Third-Order Inverse Bandpass Filter and Its Application as an Oscillator”, Journal of Circuits, Systems, and Computers, Vol. 31, No. 5,**2022,** pp. 2250079.1- 2250079.20 (**SCI Indexed**).
5. **P.Gupta** and R. Pandey, “Dual Output Voltage Differencing Buffered Amplifier Based Active -C Multiphase Sinusoidal Oscillator”, IJE Transactions C: Aspects, Vol. 32, No. 6, **2021,** pp. 1438-1444 (**ESCI Indexed**).
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8. **P.Gupta,** N. Pandeyand R. Pandey, “High CMRR Wide Bandwidth Instrumentation Amplifier Based on VDBA”, Recent Advances in Electrical & Electronic Engineering, Volume 11, Issue 2, **2018,** pp.239-247 (**ESCI Indexed**).
9. **P.Gupta** and R. Pandey "Single VDBA based Multifunction Filter", International Journal of Control Theory and Applications, Vol. 10, Issue 6, **2017**, pp. 651-­661.
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PATENT PUBLISHED

1. Title of the Invention: VDBA Based First Order All Pass Filter and Its Applications, Dated- 02 July 2021.